IN THE CLAIMS

Please cancel claims 1-3, 6, 10-12, 15, 19, 22-23, 26 and 30-32.

Please amend the claims as follows.

1-7 (Cancelled)

8. (Previously Presented) An apparatus comprising:

at least one processor;

a memory coupled to the at least one processor;

a first job residing in the memory and executed by the at least one processor;

a second job residing in the memory and executed by the at least one processor;

an inter-job breakpoint mechanism that detects at least one condition in the first job and, in response thereto, modifies a program variable in the second job.

9. (Previously Presented) The apparatus of claim 8 wherein the inter-job breakpoint mechanism, in response to detecting the at least one condition in the first job, outputs a debug message to the second job's output.

10-16 (Cancelled)

17. (Previously Presented) A method for debugging comprising the steps of:

defining at least one condition in a first job;

defining at least one action to take on a second job;

monitoring execution of the first job;

monitoring execution of the second job; and

when the at least one condition in the first job is satisfied, modifying a program variable on the second job.

18. (Previously Presented) The method of claim 17 further comprising the step of outputting of a debug message to the second job's output when the at least one condition in the first job is satisfied.

19-27 (Cancelled)

28. (Previously Presented) A computer-readable program product comprising:

(A) an inter-job breakpoint mechanism that monitors execution of first and second jobs, and when at least one condition in the first job is satisfied, modifies a program variable on the second job; and

(B) recordable media bearing the inter-job breakpoint mechanism.

29. (Previously Presented) The program product of claim 28 wherein the inter-job breakpoint mechanism, in response to detecting the at least one condition in the first job, outputs a debug message to the second job's output.

30-32 (Cancelled)